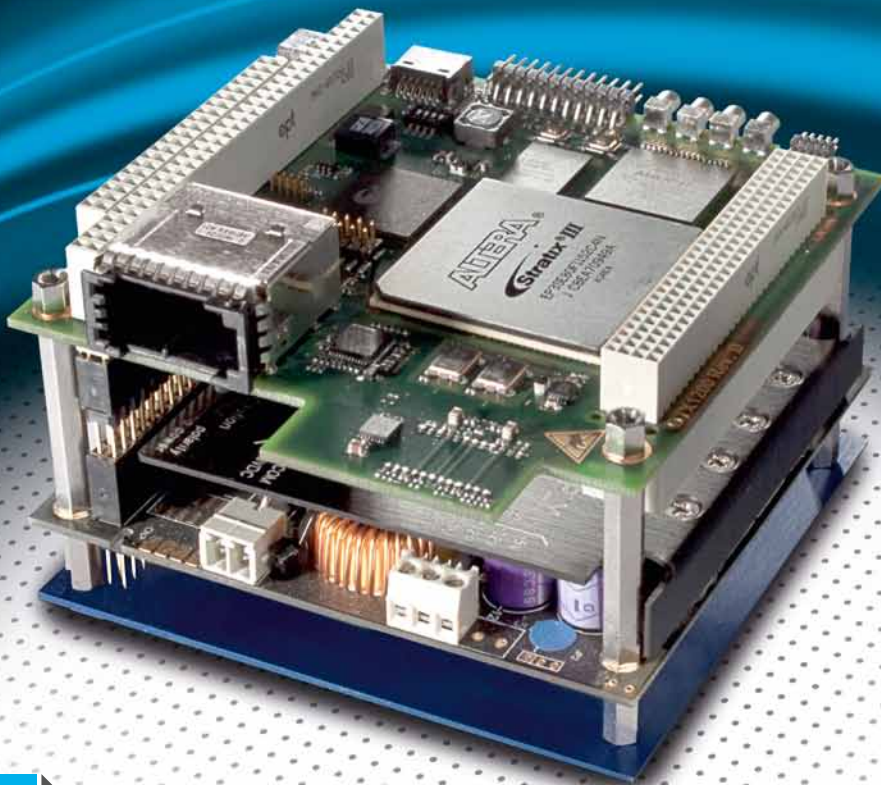


→ Vehicle Networking Tools

ONTORIX MOST 150 SUITE

EVALUATION OF MOST 150 AND INIC 150 (OS81110)



HIGHLIGHTS

With the Ontorix MOST 150 Suite it is possible to evaluate MOST 150 as well as INIC 150 (OS81110) comprehensively. By using an extremely powerful FPGA (Altera Stratix III) and completely wiring all INIC pins to the FPGA, the INIC can be operated and tested in all pin configurations. The hardware design was optimized for high flexibility and ultimate performance. The integrated soft core processor NIOS II is programmed via a USB connection from the user's PC in C/C++. A convenient API allows easy programming.

- Operation of INIC 150 in all eight pin configurations is possible
- Direct access to all INIC ports
- Control of INIC by FPGA via I²C, MLB 3 pin or MLB 6 pin
- Comprehensive observation of INIC
- Free interconnectivity of all optical ports and the HF connections
- Free programming in C/C++ with Eclipse IDE
- Off-line operation possible (without PC)

TECHNICAL DATA

Product Properties

- Hardware platform in PC/104+ format
- INIC 150 (OS81110)
- Altera Stratix III FPGA
- 4 MB RAM port
- MOST 150 port (THM), 2 more ports optional (SMD)
- JTAG interface
- 16 GPIO interfaces
- 4 HF interfaces
- Debug header for INIC Explorer
- MediaLB Analyzer header for MLB 3 pin
- Stable power supply by DC/DC converter
- Programming interface to PC via USB Blaster
- Eclipse based development environment for NIOS II

System Interfaces

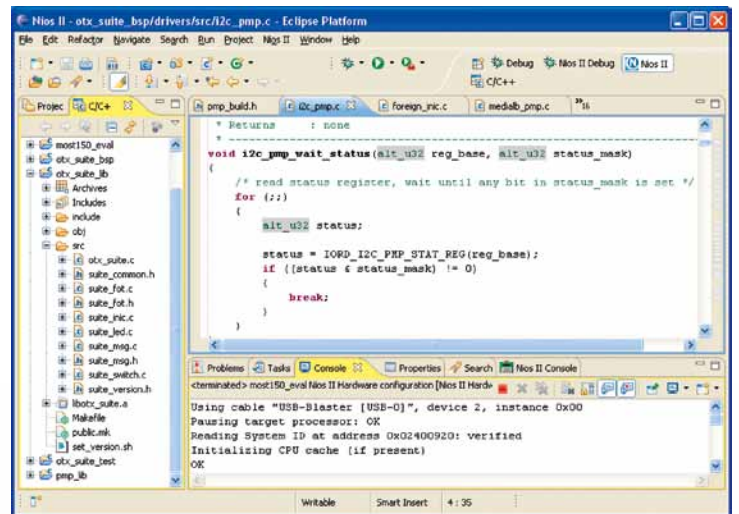
- MOST 150 Board (OTX1200) in PC/104+ format
- DC/DC converter board for stable power supply
- 12 V power supply
- USB Blaster as programming interface to the PC
- Optical and electrical cables
- Installation manual
- Software CD
- Transport case

Use Cases

- Evaluation of MOST 150 and INIC 150 (OS81110)
- Performance measurements
- Feasibility studies
- Security tests
- Demonstration platform
- Test platform for MOST NetServices

Programming Environment

With the Eclipse-based programming environment NIOS II IDE a mature development environment for programming in C/C++ is available. A complete API layer enables access to all hardware components.



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